

Product Specifications

| Customer | Standard | | | | | | | |
|-------------|---|------------------------|-------------------------|--|--|--|--|--|
| Description | 4.3" TFT LCD Module | | | | | | | |
| Model Name | LR043HC212 | LR043HC212 | | | | | | |
| Date | 2007/08/27 | 2007/08/27 | | | | | | |
| Doc. No. | | | 07 | | | | | |
| Revision | 04 | | , N | | | | | |
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| Customer Ap | oroval | R | | | | | | |
| | 4 | X ^O | | | | | | |
| Date | | | | | | | | |
| | e represents that the plications are accepted | roduct specifications, | testing regulation, and | | | | | |
| | (FID) | | | | | | | |
| (| Engin | eering | | | | | | |
| Check | Date | Prepared | Date | | | | | |
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| Engineering | | | | | | |
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| Check | Date | Prepared | Date | | | |
| HILL | | | | | | |

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Preliminary

Rev: 04



RECORD OF REVISIONS

| Date | Page | Description |
|---------------|----------------------------------|--|
| 2007/01/03 | All | New Creation |
| 2007/01/18 | 16 | Modify Test Conditions |
| 2007/05/22 | 4 | Modify Absolute Maximum Ratings |
| | 13 | Modify LCM Pin Definition |
| | 16 | Modify Test Conditions |
| | 17 | Modify Outline Drawing |
| | 18 | Add Package Information |
| | | 24 |
| 2007/08/27 | 1 | Model Change to HC212 |
| | 9 | Brightness and CR Upgrade to 300 and 400. |
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1. SUMMARY

LR043HC212 is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a touch panel, TFT LCD panel, driver ICs, FPC and a backlight unit. The following table described the features of LR043HC212.

2. FEATURES

High Resolution: 391,680 Dots (480 RGB x 272). Application: Portable Navigation PMP (Personal Multimedia Player), MP4 application product DVB-S

3. GENERAL SPECIFICATIONS

| Parameter | | Specifications | Unit |
|----------------------|-----------|---------------------------|------|
| Screen Size | | 4.3(Diagonal) | inch |
| Display Format | | 480 RGB x 272 | Dot |
| Active Area | | 95.04(H) x 53.856(V) | mm |
| Pixel Pitch | | 0.198(H) x 0.198(V) | mm |
| Surface Treatment | | Touch panel , 10% haze | |
| Pixel Configuration | | RGB-Stripe | |
| Outline Dimension | | 105.5(W) x 67 2(H) x 5(D) | mm |
| Weight | | TBD | g |
| View Angle Direction | | 6 o'clock | |
| | Operation | -20~70 | °C |
| Temperature Range | Storage | -30~80 | °C |

4. ABSOLUTE MAXIMUM RATINGS

| ltem | Symbol | Val | ues | Unit | Condition |
|---------------------|----------|------|-----------|------|-----------|
| nem | - Cymbol | Min. | Max. | | Condition |
| Power Voltage | D_VDD | 0.3 | 5.0 | V | VSS=0 |
| Logic Input Signal | Vin | -0.3 | D_VDD+0.3 | V | |
| Logic Output Signal | Vout | -0.3 | D_VDD+0.3 | V | |

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above.



5. ELECTRICAL CHARACTERISTICS

5.1. Operating conditions:

| ltem | Symbol | Values | | | Unit | Remark |
|---------------------------|--------|--------|------|------|------|---------|
| item | Symbol | Min. | Тур. | Max. | Unit | Rellark |
| Digital Power Supply | VCC | 2.7 | 3.0 | 3.3 | V | |
| Digital Operating Current | lcc | - | 30 | - | mA | |
| Frame frequency | fFrame | - | 60 | 90 | Hz | |
| Dot Data Clock | DCLK | - | 9.0 | 15 | MHz | |
| Power Consumption | PLCD | - | 99 | - | mW | 1 |

5.2 LED driving conditions

| .2 LED driving condition | ons | | | | | OL, |
|--------------------------|--------|------|--------|------|------|--------|
| Item | Symbol | | Values | 6 | Unit | Remark |
| item | Symbol | Min. | Тур. | Max. | Unit | Nemark |
| Power Consumption | PLED | - | 476 | - | mW | |
| LED Current | lf | - | 20 | - | mA | |
| Backlight Voltage | Vb | - | 23.8 | - | V | |

Note 1 : Ta = 25℃

Note 2 : Brightess to be decreased to 50% of the initial value

6. DC CHARATERISTICS

| Parameter | Symbol | . 2 | Rating | | Unit | Condition |
|---------------------------|--------|-----------|--------|-----------|------|-----------|
| Falameter | Symbol | Min. | Тур. | Max. | Unit | Condition |
| Low level input voltage | VIL | 0 | - | 0.3*D_VDD | V | |
| Hight level input voltage | VIH | 0.7*D_VDD | - | D_VDD | V | |
| Analog operating current | | - | (9) | (12) | mA | |
| Logic operating current | | - | (3) | (5) | mA | |
| CHILIN | | | | | | |



7. AC CHARATERISTICS 7.1 AC Timing Characteristics

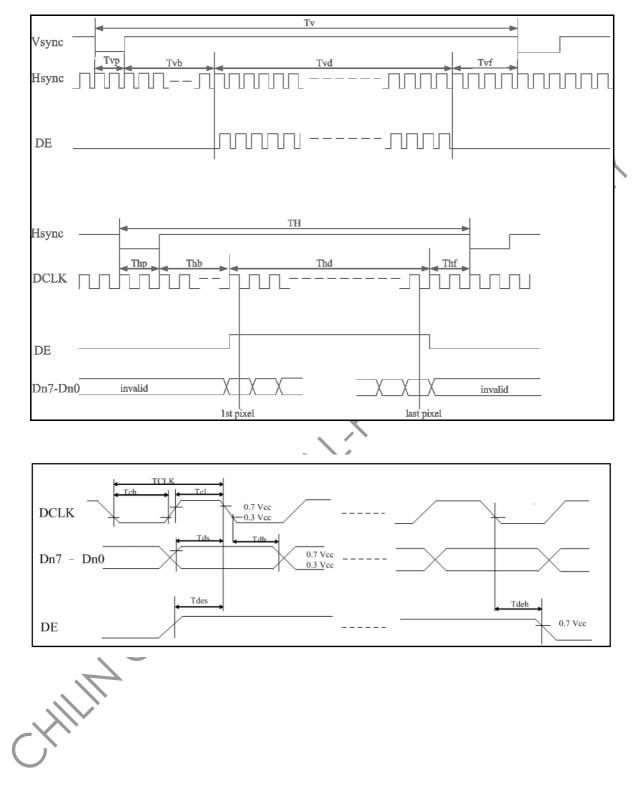
| Signal | ltem | Symbol | Min | Тур | Мах | Unit | Note |
|--------|----------------|--------|----------|-----|-----|------|---------------|
| | Frequency | DCLK | - | 9.0 | 15 | MHZ | |
| Dalle | Clock Period | TCLK | 66.7 | - | - | ns | |
| Dclk | High Time | Tch | 26.7 | - | - | ns | |
| | Low Time | Tcl | 26.7 | - | - | ns | |
| Data | Setup Time | Tds | 10 | - | - | ns | \mathcal{L} |
| Dala | Hold Time | Tdh | 10 | - | - | ns |)` |
| | Setup Time | Tdes | 10 | - | - | ns | |
| DE | Hold Time | Tdeh | 10 | - | - 1 | ns | |
| | Period | TH | - | 525 | | DCLK | (1) |
| | Pulse Width | Thp | 2 | 41 | 4 | DCLK | (2) |
| Hsync | Back-Porch | Thb | 2 | X | - | DCLK | (2) |
| | Display Period | Thd | - < | 480 | - | DCLK | |
| | Front-Porch | Thf | 2 | - | - | DCLK | (2) |
| | Period | Τv | <u> </u> | 286 | - | TH | |
| | Pulse Width | Тур | 2 | 10 | - | ТН | |
| Vsync | Back-Porch | Тур | 2 | 2 | - | ТН | |
| | Display Period | Tvd | - | 272 | - | ТН | |
| | Front-Porch | Tvf | 2 | 2 | - | ТН | |

Note1: Thd=480 DCLK, Thf=2 DCLK, Thp= 41 DCLK, Thb=2 DCLK 525 DCLK= 480 + 2 + 41 + 2 (DCLK)

Note2: Thf+ Thp+ Thb >44



7.2 AC Timing Diagrams

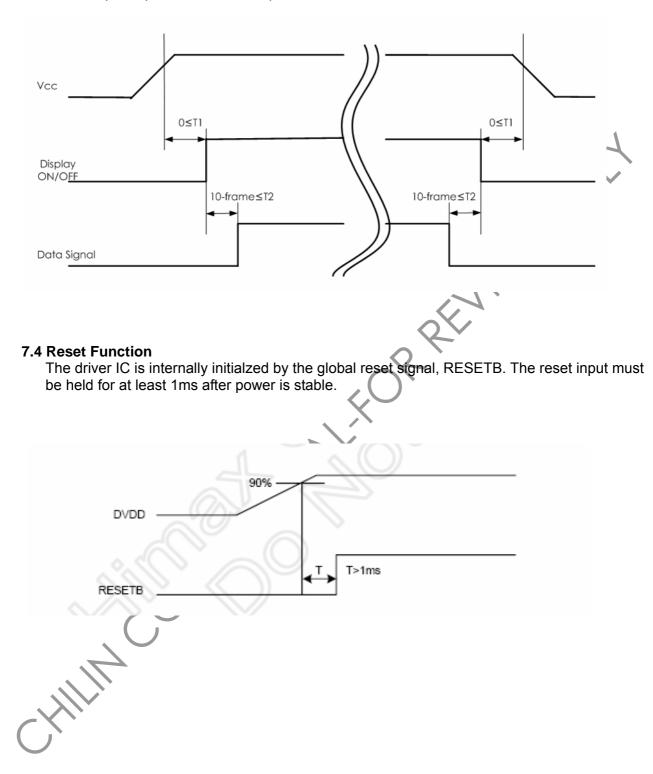


Preliminary



7.3 Power Sequence

The LCD panel power ON/OFF sequence is as below.

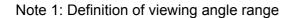


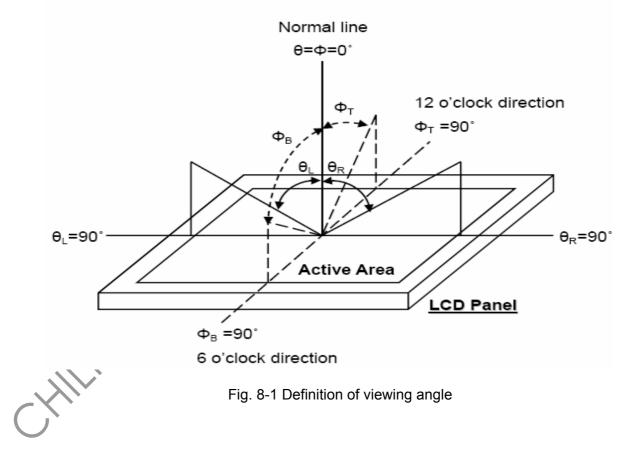


a=25±2°C, ILED=20mA

8. OPTICAL CHARATERISTIC

| ltem | | Symbol | Condition | Min | Тур | Max | Unit | Note |
|-----------------|--------|--------|-------------------------------------|------|------|------|--------|------|
| Brightness | 6 | | | 250 | 300 | - | cd/m2 | |
| Response tir | no | TR | Θ=0 | - | 15 | I | ms | (2) |
| | ne | TF | 0-0 | - | 35 | I | ms | (2) |
| Contrast rat | tio | CR | At optimized viewing angle | 300 | 400 | - | - | (3) |
| Color | White | Wx | Θ=0 | 0.26 | 0.31 | 0.36 | % | |
| Chromaticity | vvinte | Wy | 0-0 | 0.28 | 0.33 | 0.38 | 70 | (4) |
| | Hor. | ΘR | | 50 | 60 | - | | 7 |
| | 1101. | ΘL | | 50 | 60 | - | Dograa | (5) |
| Viewing Angle – | Ver. | φH | CR≧10 | 40 | 50 | - | Degree | (5) |
| | vel. | φL | | 45 | 55 | - | N | |





Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 luminance meter 1.0° field of view at a distance of 50cm and normal direction.



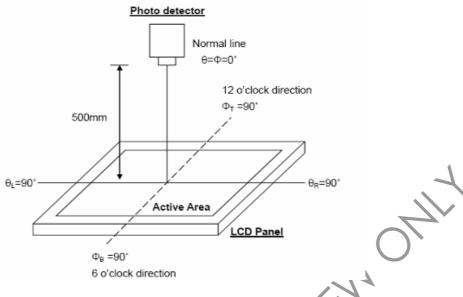
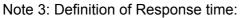
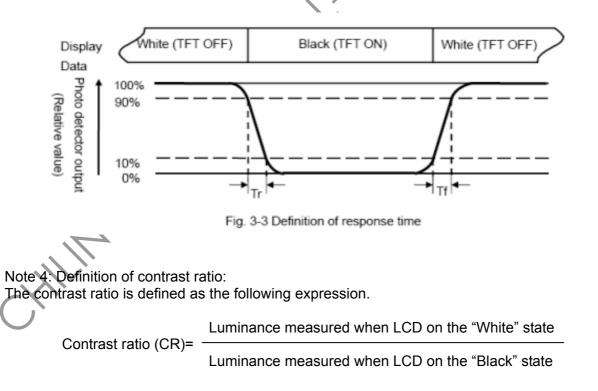


Fig. 8-2 Optical measurement system setup



The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time, Tr, is the time between photo detector output intensity changed from 90% to 10%. And fall time, Tf, is the time between photo detector output intensity changed from 10% to 90%.



Note 5: White Vi = $V_{i50} \pm 1.5V$ Black Vi = $V_{i50} \pm 2.0V$ " \pm " means that the analog input signal swings in phase with VCOM signal. " \pm " means that the analog input signal swings out of phase with VCOM signal. The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.



Doc. No.

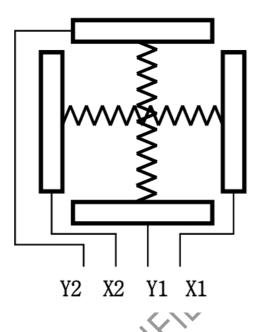
Note 6: Definition of color chromaticity (CIE 1931) Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

| | Brightness (min) | |
|---------------------------|------------------|---------|
| Note 8 : Uniformity (U) = | | -x 100% |
| | Brightness (max) | |

9 TOUCH PANEL 9.1 Block diagram





Top View

- ${\tt X}$: Upper electrode
- Y : Lower electrode

| Pin | Symbol | | Function |
|------------|--------|--------|--|
| 1 X1 Right | | Right | Right electrode - differential analog |
| 2 Y1 | | Bottom | Bottom electrode - differential analog |
| 3 | X2 | Left | Left electrode – differential analog |
| 4 | Y2 | Тор | Top electrode - differential analog |



9.2 Absolute maximun ratings

| Item | Symbol Valu | | ues- | Unit | Condition |
|-----------------------|-------------|------------|---------|------|-----------|
| | Symbol | Symbol Min | | Unit | |
| Power Voltage | Vcc | -0.3 | 7.0 | V | GND=0 |
| Input Singal Voltage | Vi | -0.3 | Vcc+0.3 | V | |
| LED Reverse Voltage | Vr | - | TBD | V | One LED |
| LED Forward Current | lr | - | TBD | mA | One LED |
| LED Power Dissipation | Pd | - | TBD | mW | One LED |

9.3 Electrical characteristics

| Item | Min. | Тур. | Max. | Unit | Note |
|-----------------------|------|------|------|------|------------------|
| Linearity | - | - | 1.5 | % | |
| Terminal Resistance | 200 | - | 900 | Ω | X (Film side) |
| | 200 | - | 900 | Ω | Y (Film side) |
| Insulation resistance | 20 | - | - | MΩ | At DC 25V, 60sec |
| Voltage | - | - | 5 | V | DC |
| Chattering | - | - | 10 | ms | ON/OFF |
| Transparency | - | 80 | 2 | % | Non-glare |
| Haze rate | - | - | 10 | % | |

Note: Do not operate it with a thing except a polyacetal pen (tip R0.8mm or less) or a finger, especially those with hard or sharp tips such as a ball point pen or a mechanical pencil.

9.4 Mechanical characteristics

| Item | Min. | Тур. | Max. | Unit | Note |
|----------------------------------|------------------|------|------|------------|-----------------------|
| Activation force | | - | 100 | g | (1) |
| Durability-surface scratching | Write 100,000 | - | - | characters | (2) |
| Durability-surface pitting | 1,000,000 | - | - | touches | (3) |
| Surface hardness | 3 | - | - | Н | JIS K5400, ASTM D3363 |

Note1: Stylus pen Input : R0.8mm polyacetal pen or Finger

Note2: Measurement for Surface area

- Scratch 100,000 times straight lines on the Film with a stylus change every 20,000 times.
- Force= 150gf. Speed= 60mm/sec.
- -Stylus= R0.8 polyacetal tip.

Note3: Pit 1,000,000 times on the Film with a R8.0 silicon rubber



10. INTERFACE 10.1. LCM PIN Definition

| Pin | Symbol | I/O | Function | Remark | | |
|-----|--------|-----|-----------------------|------------|--|--|
| 1 | DGND | Ι | Digital Ground | | | |
| 2 | DGND | I | Digital Ground | | | |
| 3 | VCC | Ι | Power Supply | | | |
| 4 | VCC | Ι | Power Supply | | | |
| 5 | R0 | Ι | Red Data Bit0 | | | |
| 6 | R1 | Ι | Red Data Bit1 | | | |
| 7 | R2 | Ι | Red Data Bit2 | \bigcirc | | |
| 8 | R3 | I | Red Data Bit3 | N | | |
| 9 | R4 | Ι | Red Data Bit4 | ン | | |
| 10 | R5 | Ι | Red Data Bit5 | | | |
| 11 | R6 | Ι | Red Data Bit6 | | | |
| 12 | R7 | Ι | Red Data Bit7 | | | |
| 13 | G0 | Ι | Green Data Bit0 | | | |
| 14 | G1 | Ι | Green Data Bit1 | | | |
| 15 | G2 | Ι | Green Data Bit2 | | | |
| 16 | G3 | Ι | Green Data Bit3 | | | |
| 17 | G4 | Ι | Green Data Bit4 | | | |
| 18 | G5 | Ι | Green Data Bit5 | | | |
| 19 | G6 | Ι | Green Data Bit6 | | | |
| 20 | G7 | Ι | Green Data Bit7 | | | |
| 21 | B0 | | Blue Data Bit0 | | | |
| 22 | B1 | 4 | Blue Data Bit1 | | | |
| 23 | B2 | ノ | Blue Data Bit2 | | | |
| 24 | B3 | I | Blue Data Bit3 | | | |
| 25 | B4 | Ι | Blue Data Bit4 | | | |
| 26 | B5 | - | Blue Data Bit5 | | | |
| 27 | B6 | _ | Blue Data Bit6 | | | |
| 28 | B7 | Ι | Blue Data Bit7 | | | |
| 29 | DGND | Ι | Digital Ground | | | |
| 30 | DCLK | Ι | Dot Data Clock | | | |
| 31 | DISP | I | Display On/Off | Note 1 | | |
| 32 | Hsync | I | Horizontal Sync Input | | | |
| 33 | Vsync | I | Vertical Sync Input | | | |
| 34 | DE | I | Data Enable Control | Note 2 | | |

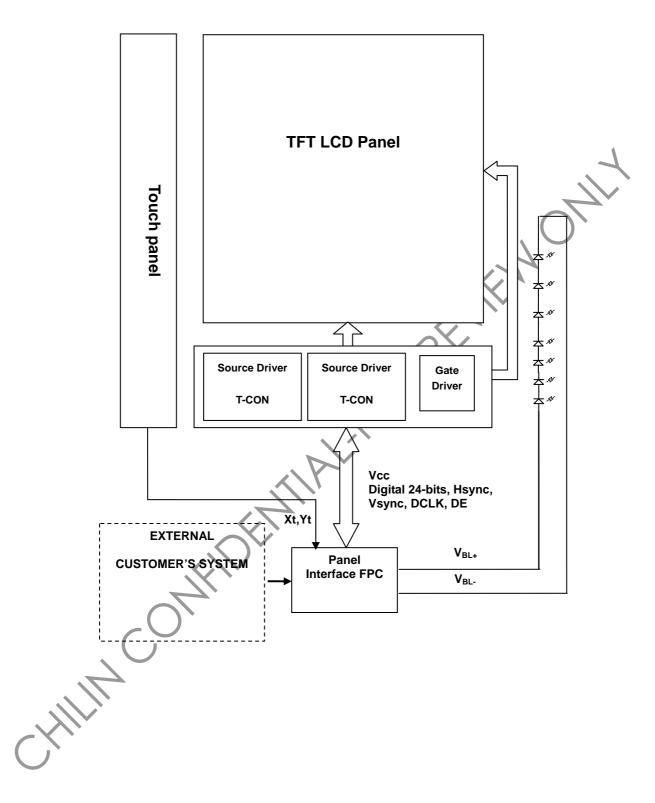


| 35 | U/D | Ι | Shift Up or Down Control | Note 3 |
|----|-------|---|--------------------------|------------|
| 36 | NC | - | Not Connection | |
| 37 | DGND | Ι | Digital Ground | |
| 38 | DGND | Ι | Digital Ground | |
| 39 | X1 | Ι | Right (TP) | |
| 40 | Y1 | Ι | Bottom(TP) | |
| 41 | X2 | Ι | Left(TP) | |
| 42 | Y2 | Ι | Up(TP) | |
| 43 | DGND | Ι | Digital Ground | |
| 44 | DGND | Ι | Digital Ground | \bigcirc |
| 45 | DGND | Ι | Digital Ground | |
| 46 | VLED- | Ι | LED Ground | 7 |
| 47 | VLED+ | Ι | LED Power | |
| 48 | DGND | Ι | Digital Ground | |
| 49 | DGND | Ι | Digital Ground | |
| 50 | DGND | I | Digital Ground | |

Note1: During set to DISP=" H ", input data are valid. During set to DISP=" L ", input data are invalid and white display data is written to data register automatically.
Note2: DE=" H ": data can be access, DE=" L ": data cannot be access
Note3: U/D=" H ": UP → Down, U/D=" L " Down → UP

n, U/.







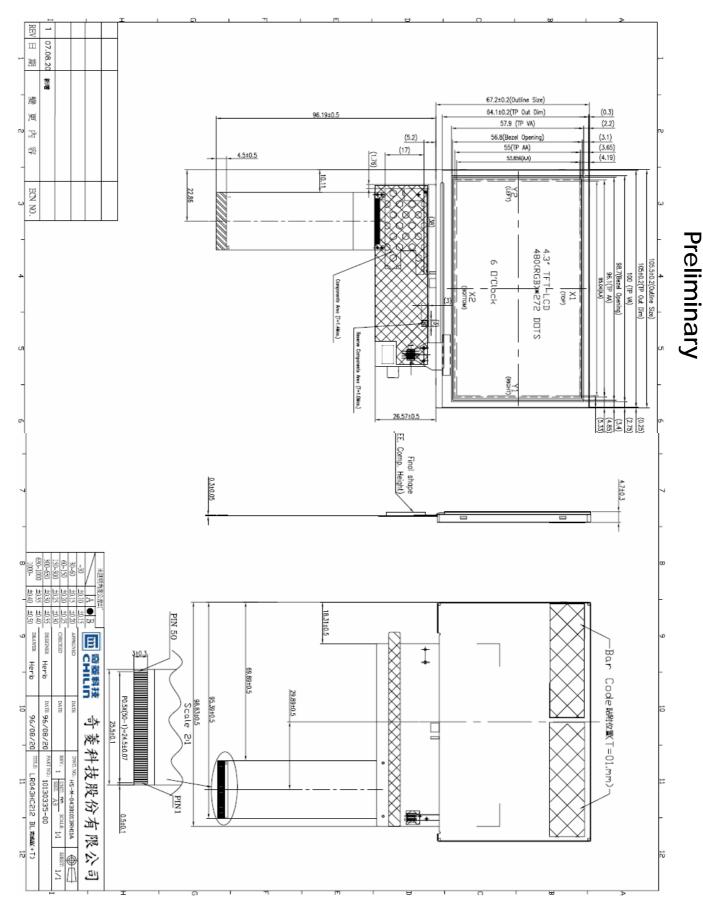
12. QUALITY ASSURANCE

| Test Item | Test Condition |
|---|---|
| High Temperature Operation | 70° C for 240 hours |
| High Temperature and High Humidity Operation | 60℃, 90%RH for 240 hours |
| Low Temperature Operation | -20 $^{\circ}$ C for 240 hours |
| High Temperature Storage | 80°C for 240 hours |
| Low Temperature Storage | -30°C for 240 hours |
| Thermal Shock | -25°C(0.5Hr) ~ +70°C(0.5Hr) for 200 cycles |
| Mechanical Shock | Shock Level : 100G Waveform : Half Sinusoidal Wave Shock Time : 6ms Number of Shocks : 3 times for each ±X, ±Y, ±Z direction |
| Vibration | Frequency Range: 10~55Hz. Amplitude:1.5 mm. Sweep Time: 11min. Test Period : 6 cycles for each direction of X,Y,Z |
| ESD | Contact / Air : ±6KV / ±8KV 450pF · 330Ω |

- Note1: The test samples have recovery time for 2 hours at room temperature before the function check. In the standard conditions, there is no display function NG issue r Insarej Kontroletek Kontrole occurred.
 - Note2: All the cosmetic specifications are judged before the reliability stress.

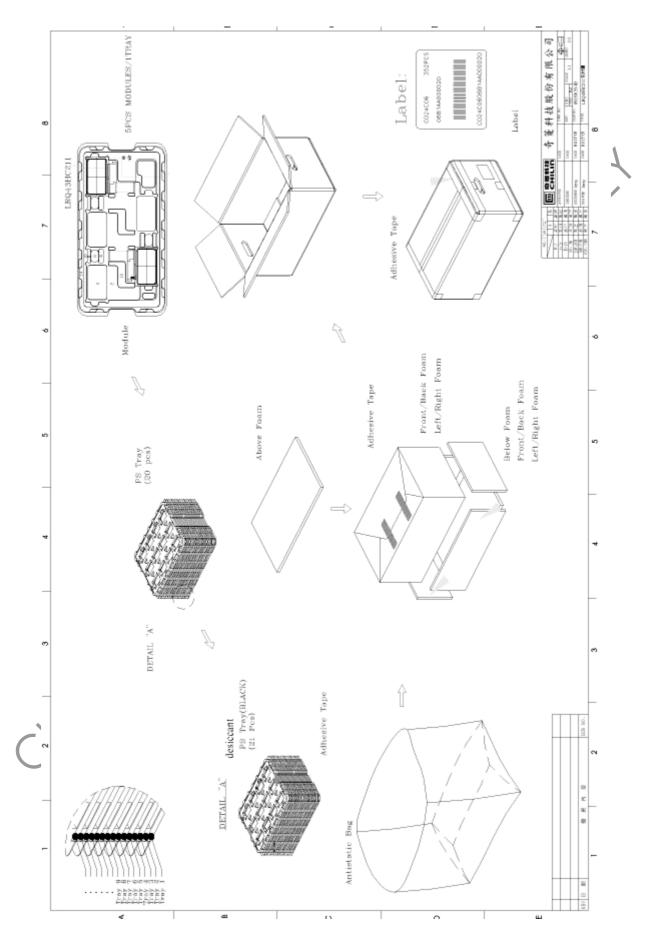
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14. PACKAGE INFORMATION





15. PRECAUTIONS

Please pay attention to the following when you use this TFT LCD module.

15.1 MOUNTING PRECAUTIONS

- (1) You must mount a module using arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module.

And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.

- (3) Please attach a transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not describe because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are determined to the polarizer)
- (7) When the surface becomes dusty, please wipe gently with adsorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

15.2 OPERATING PRECAUTIONS

- The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

15.3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

15.4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

15.5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.



(2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

15.6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. Is apt to remain on the polarizer. Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

15.7 CAUTIONS FOR INSTALLING AND ASSEMBLING

Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.

